

FIG. 1

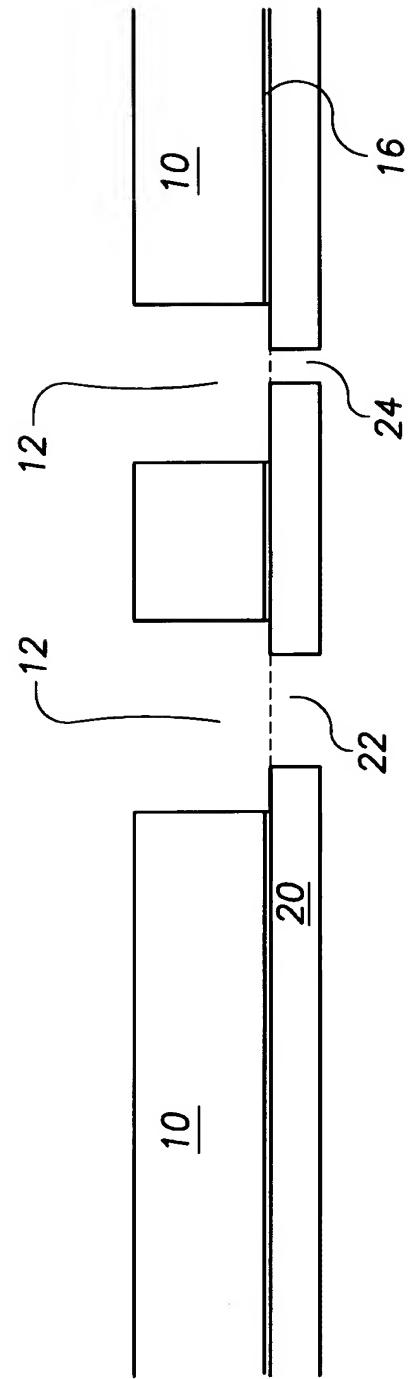


FIG. 2

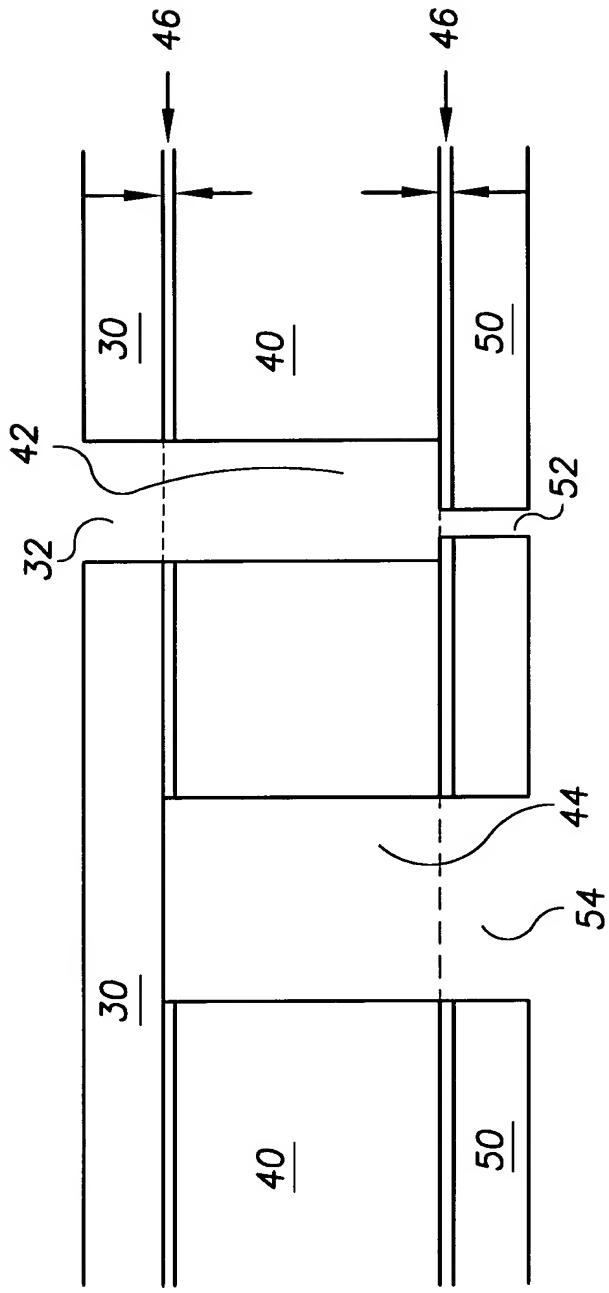
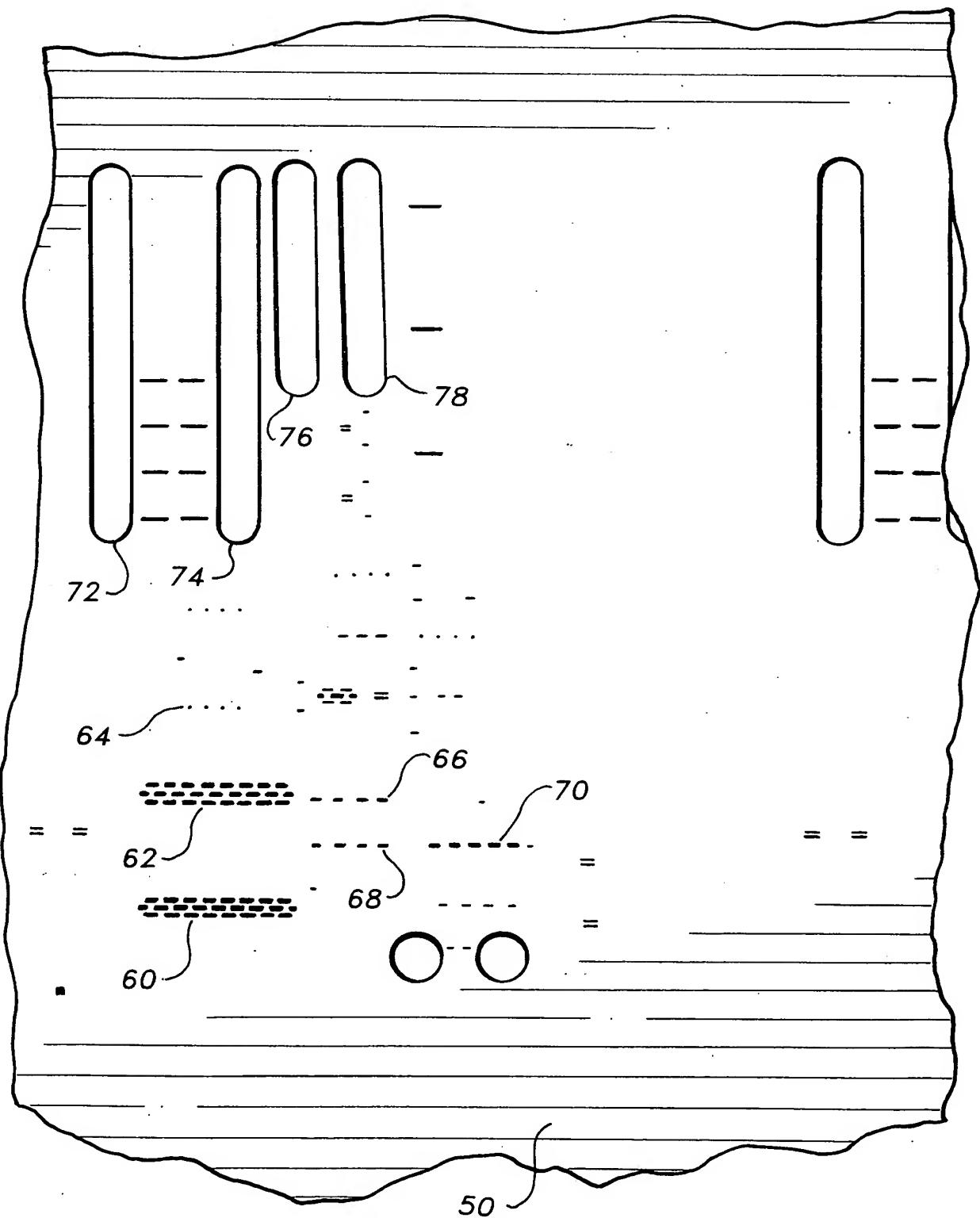
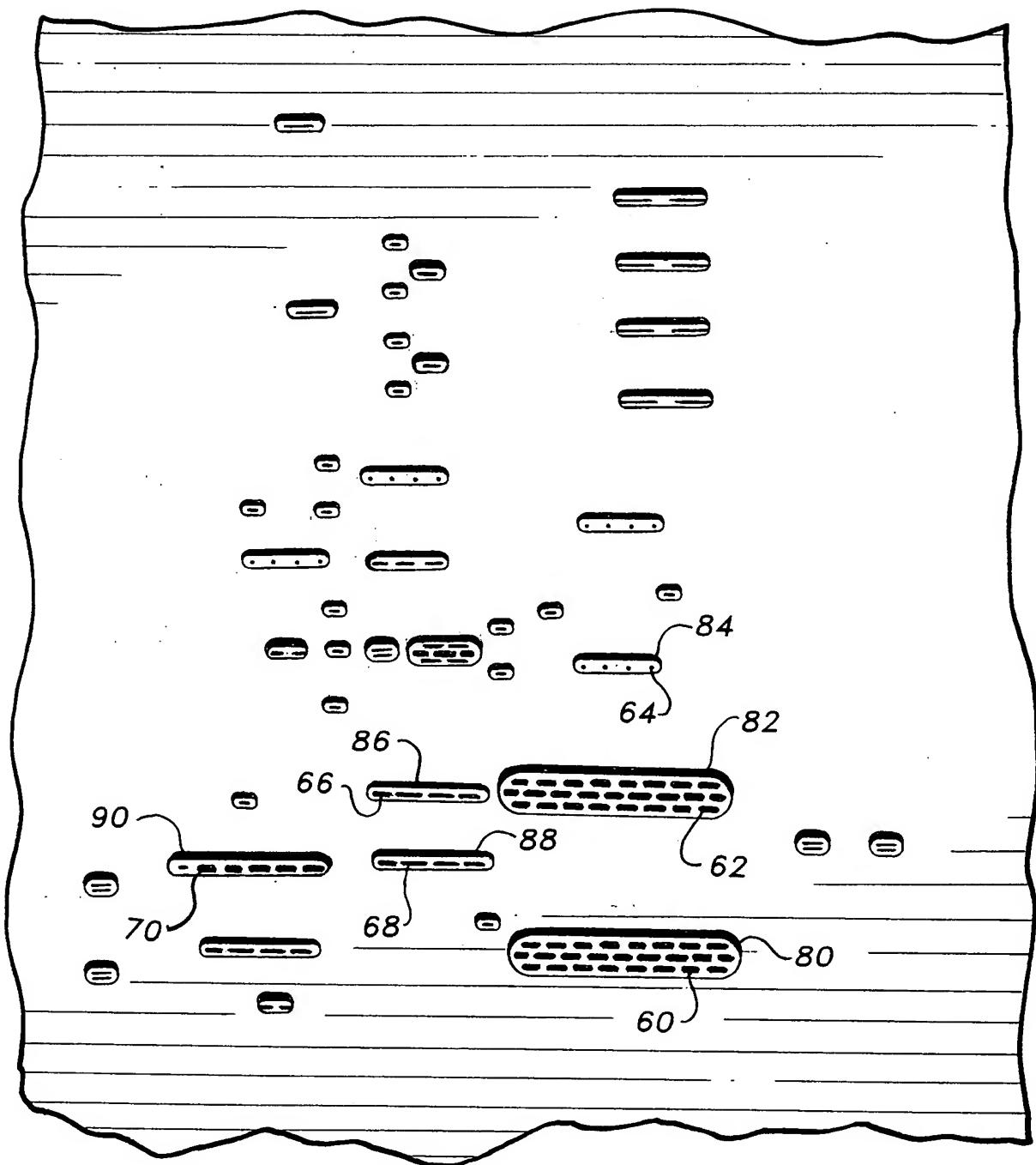


FIG. 3



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FIG. 4

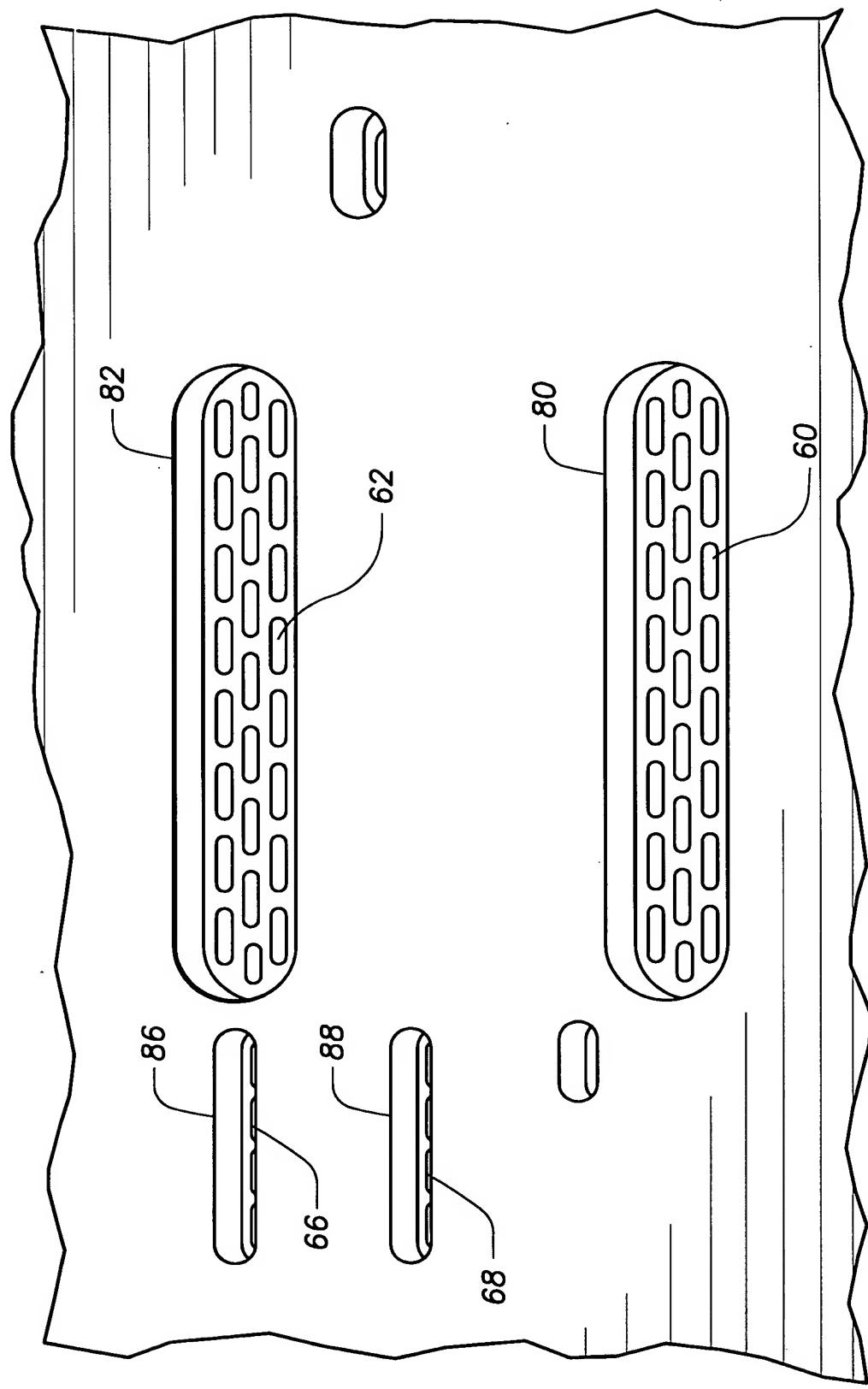


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FIG. 5



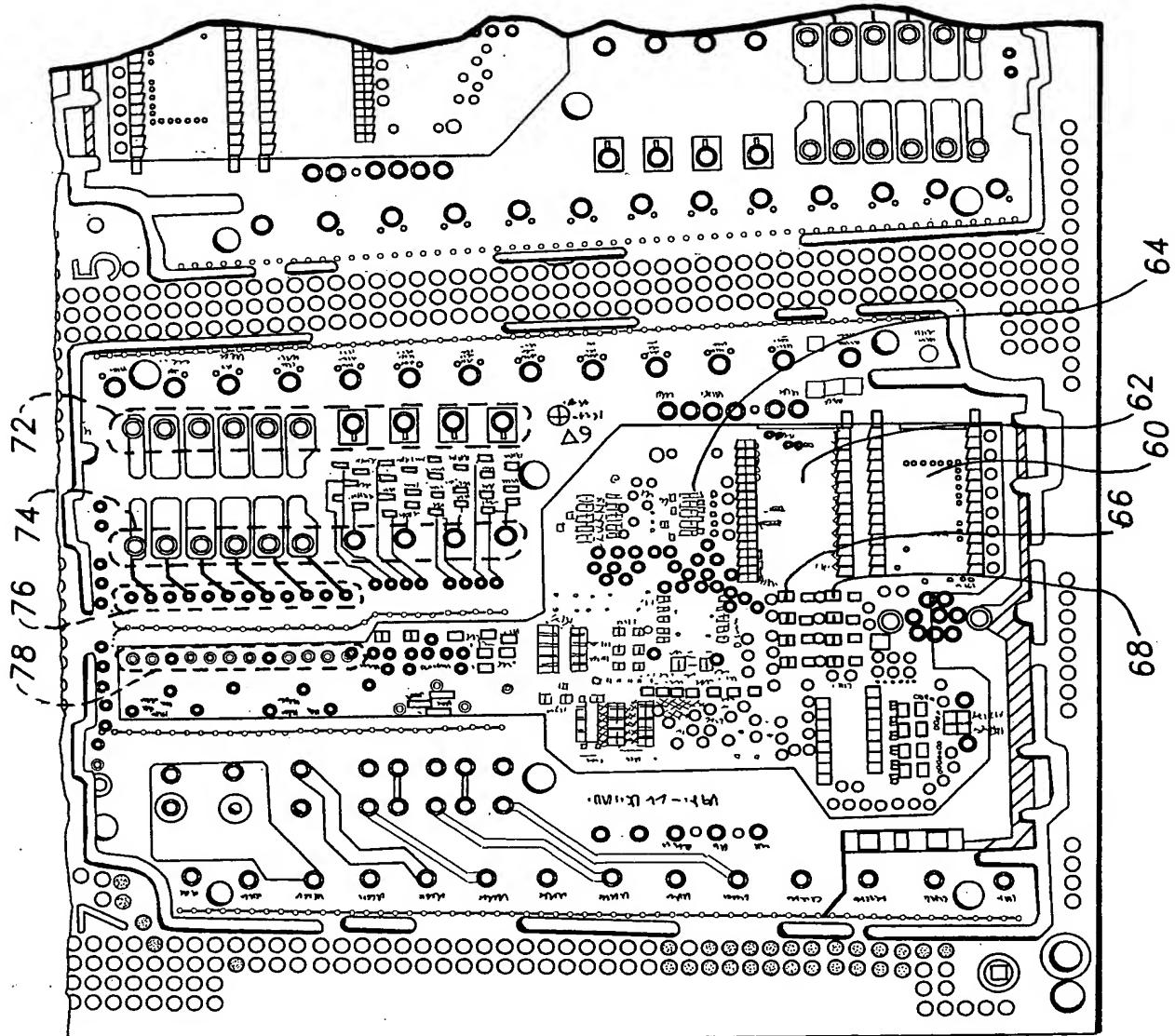
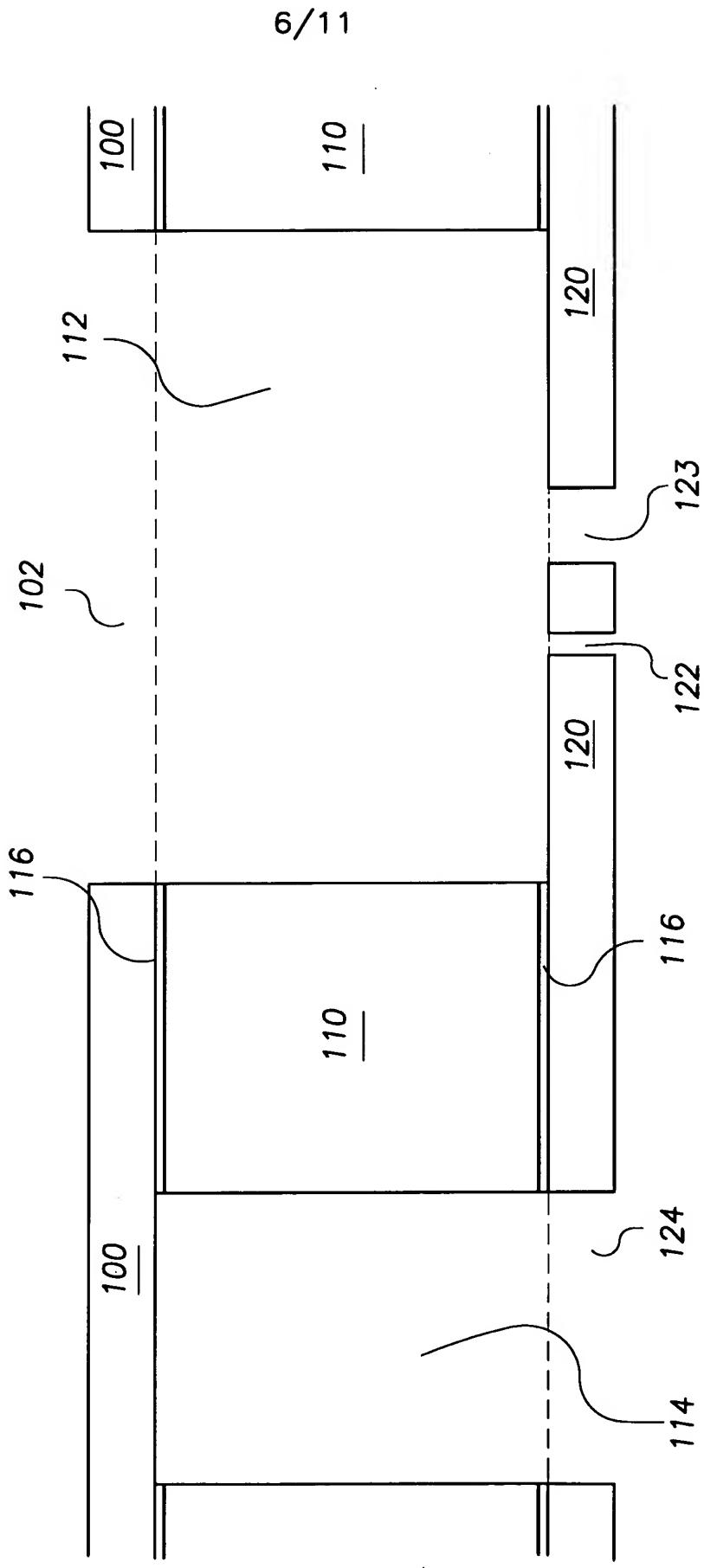


FIG. 6

FIG. 7

FIGURE 7  
Schematic diagram of a circuit board layout.



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FIG.8

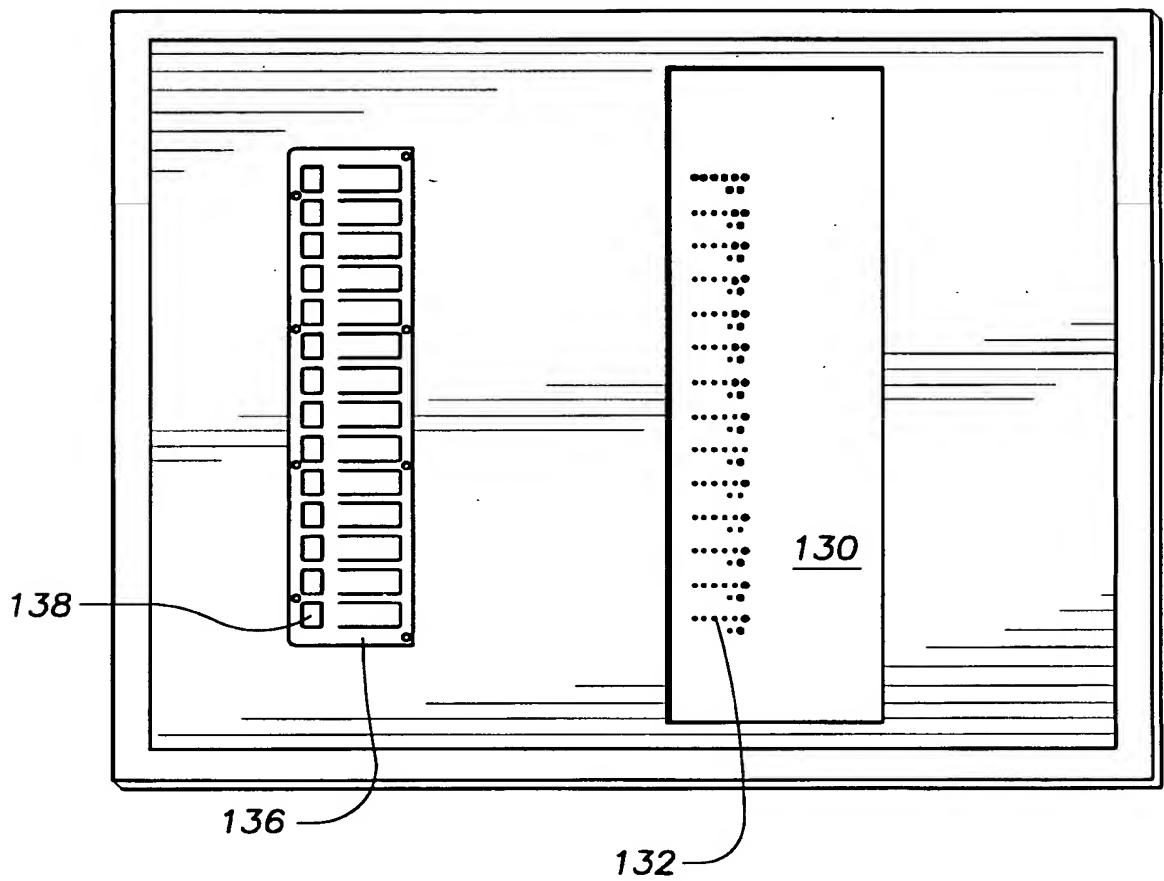
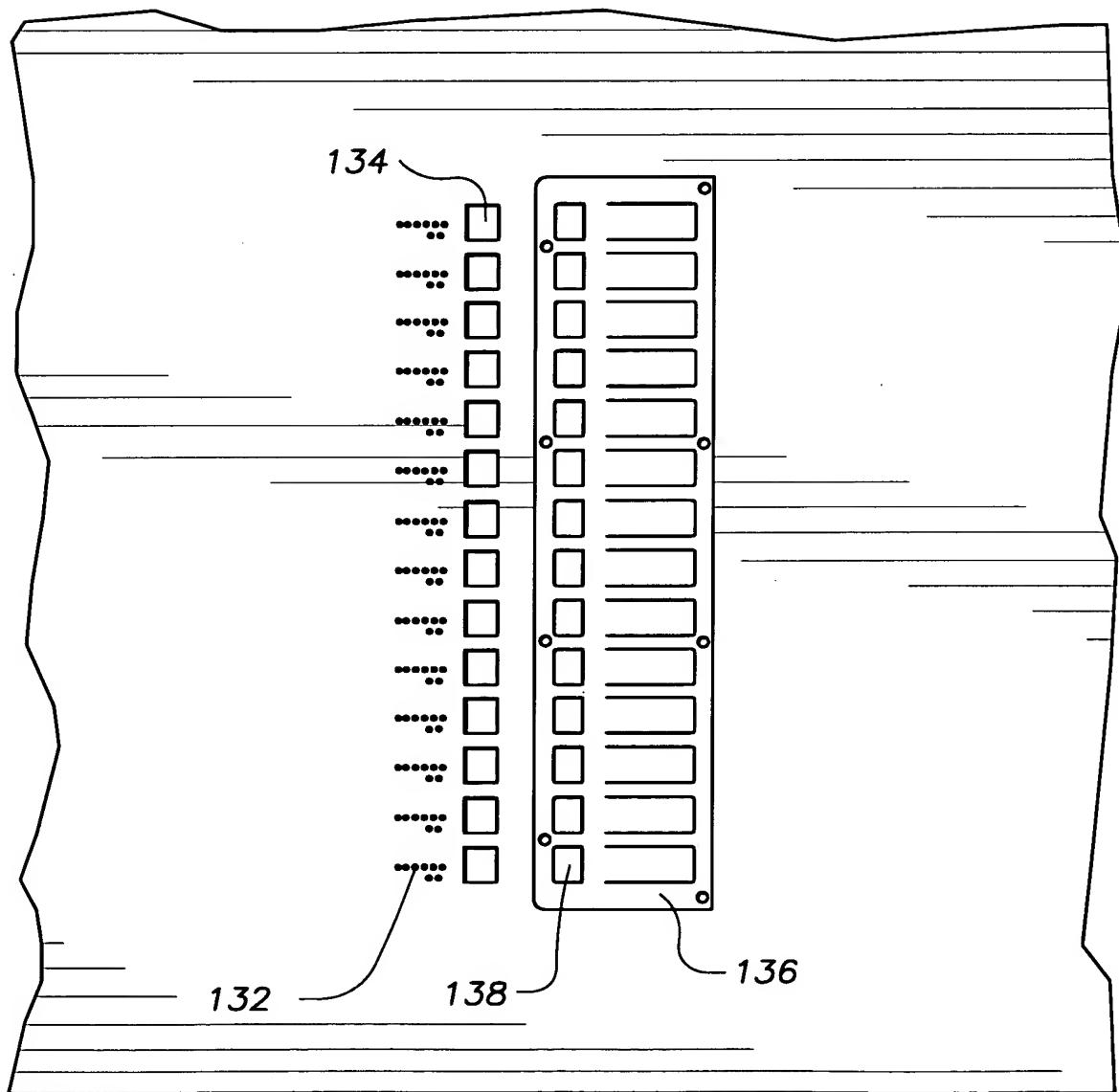


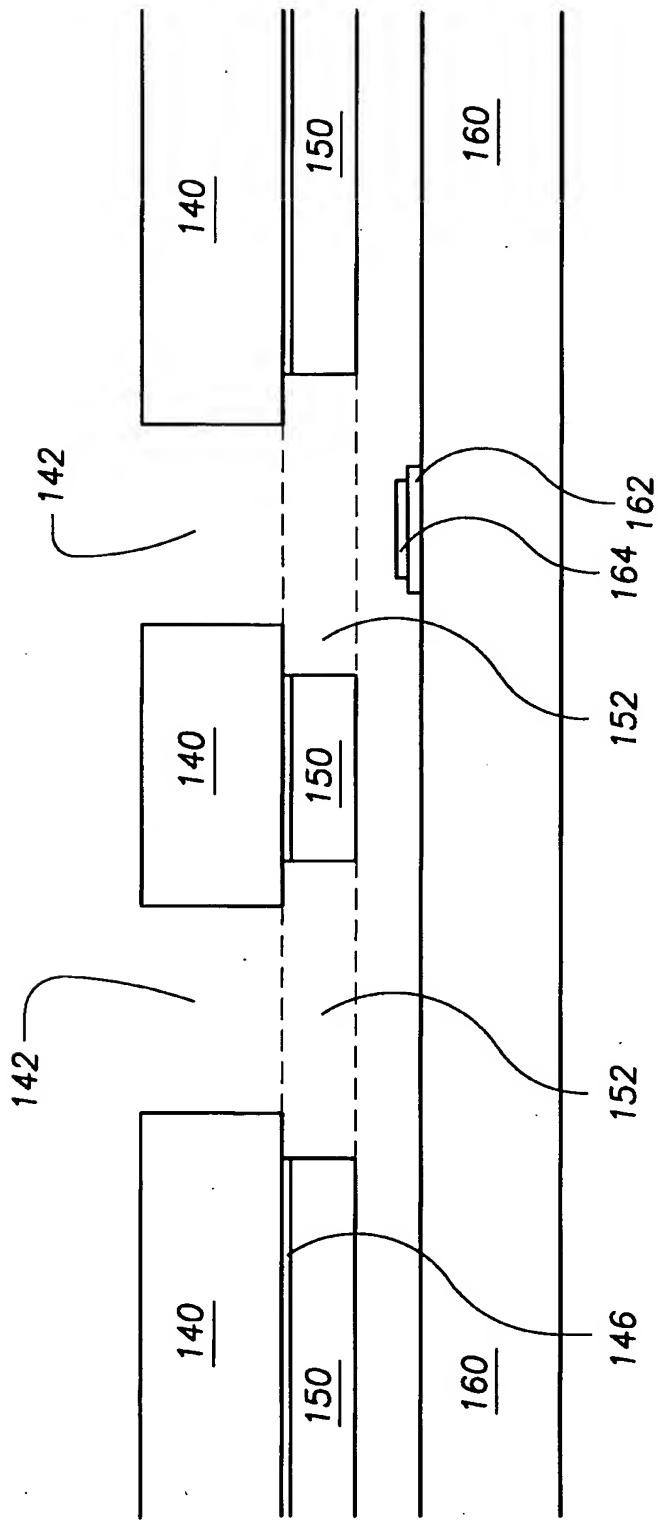
FIG.9



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FIG. 10

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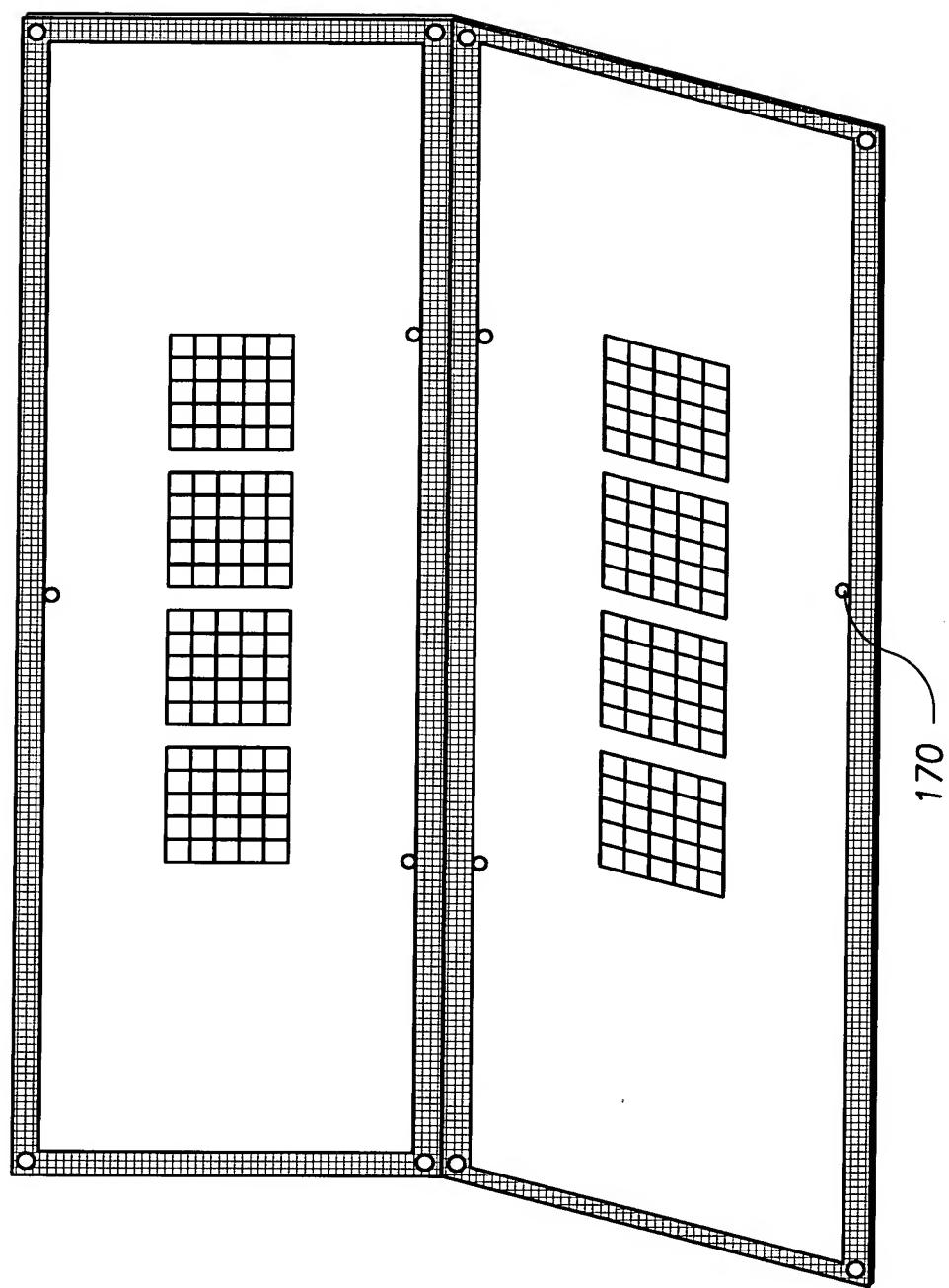


FIG. 11

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FIG. 12

